

FIG. 1

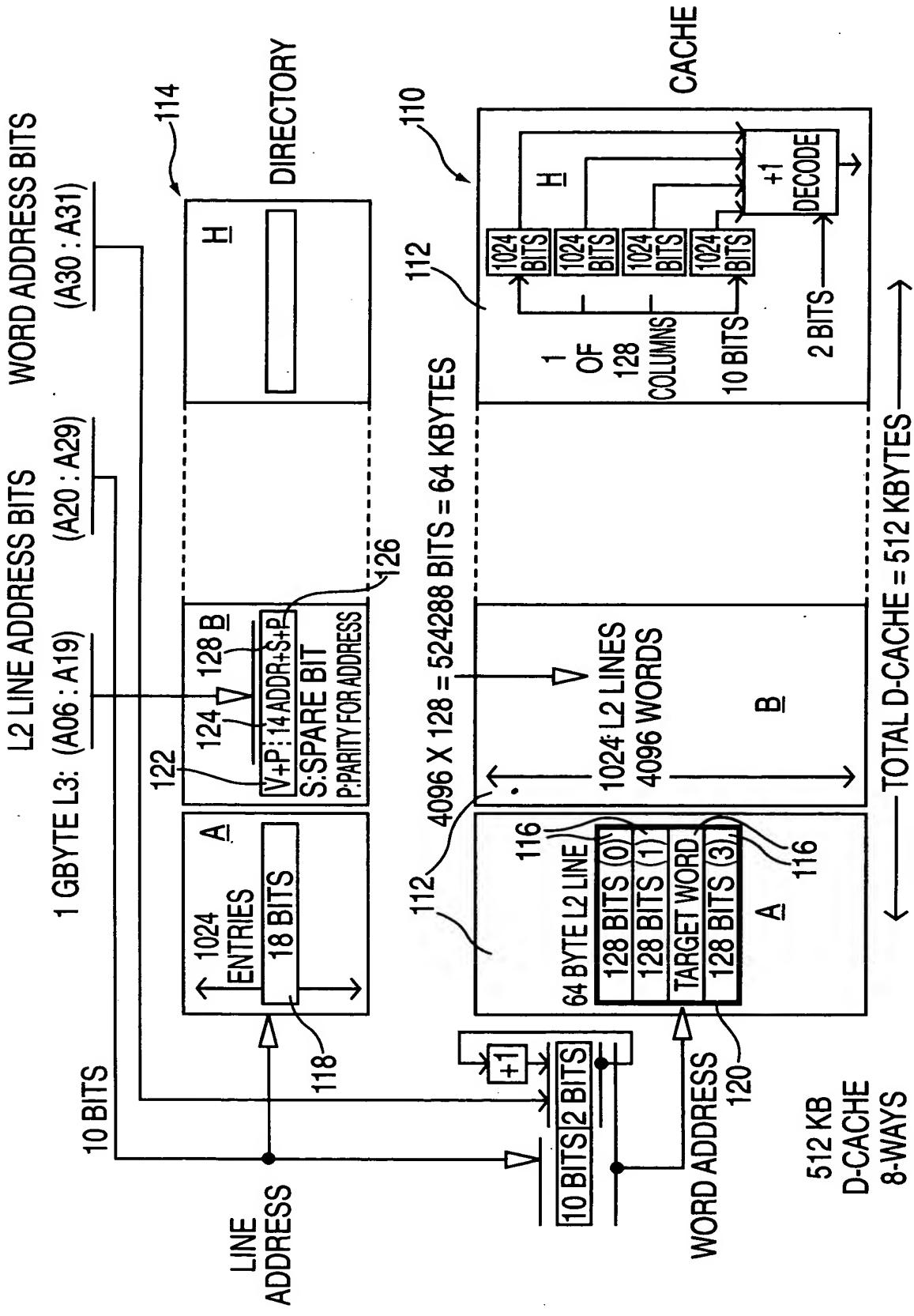


FIG. 2 128 BITS FROM 1 OF 8 COMPARTMENTS

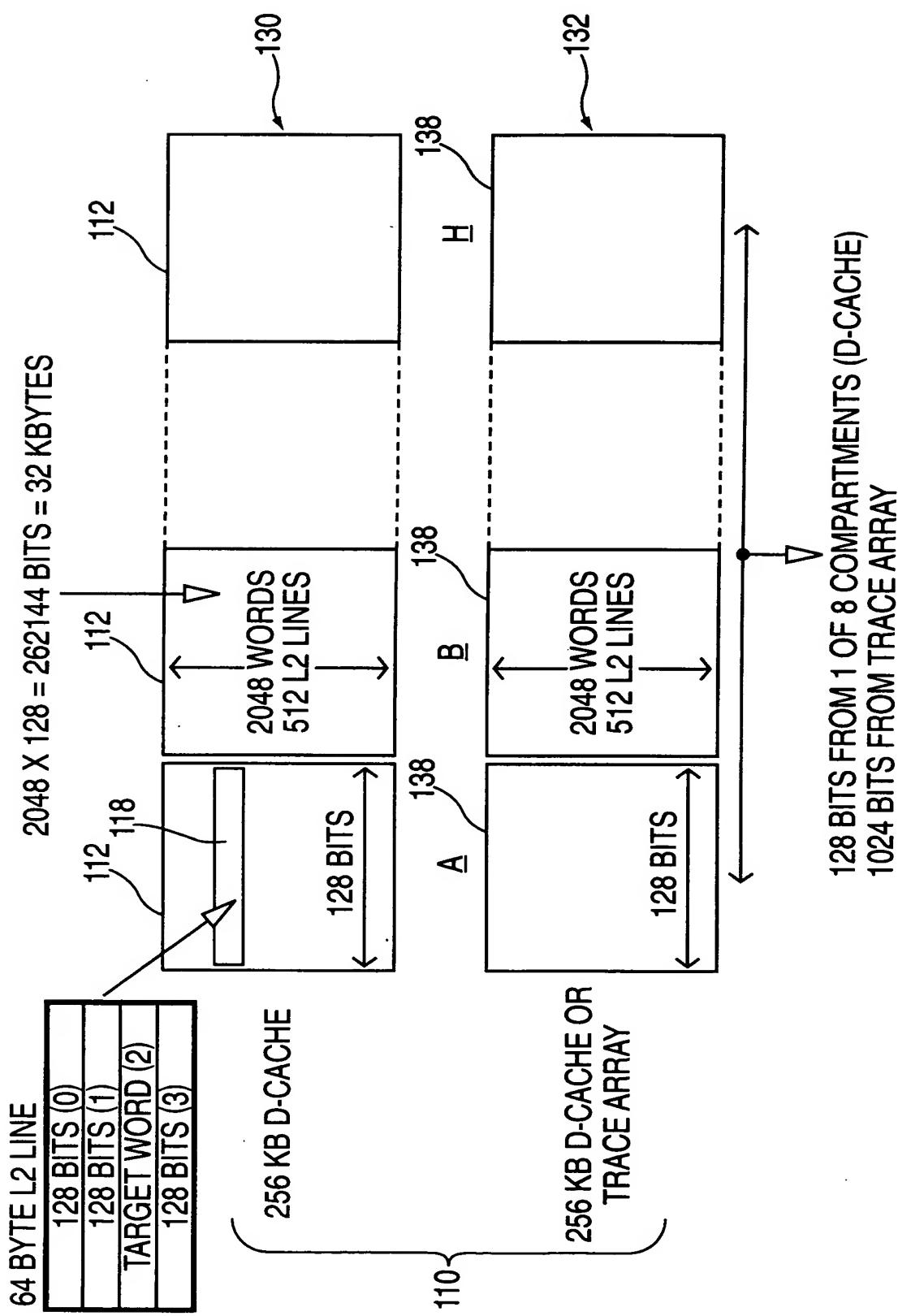
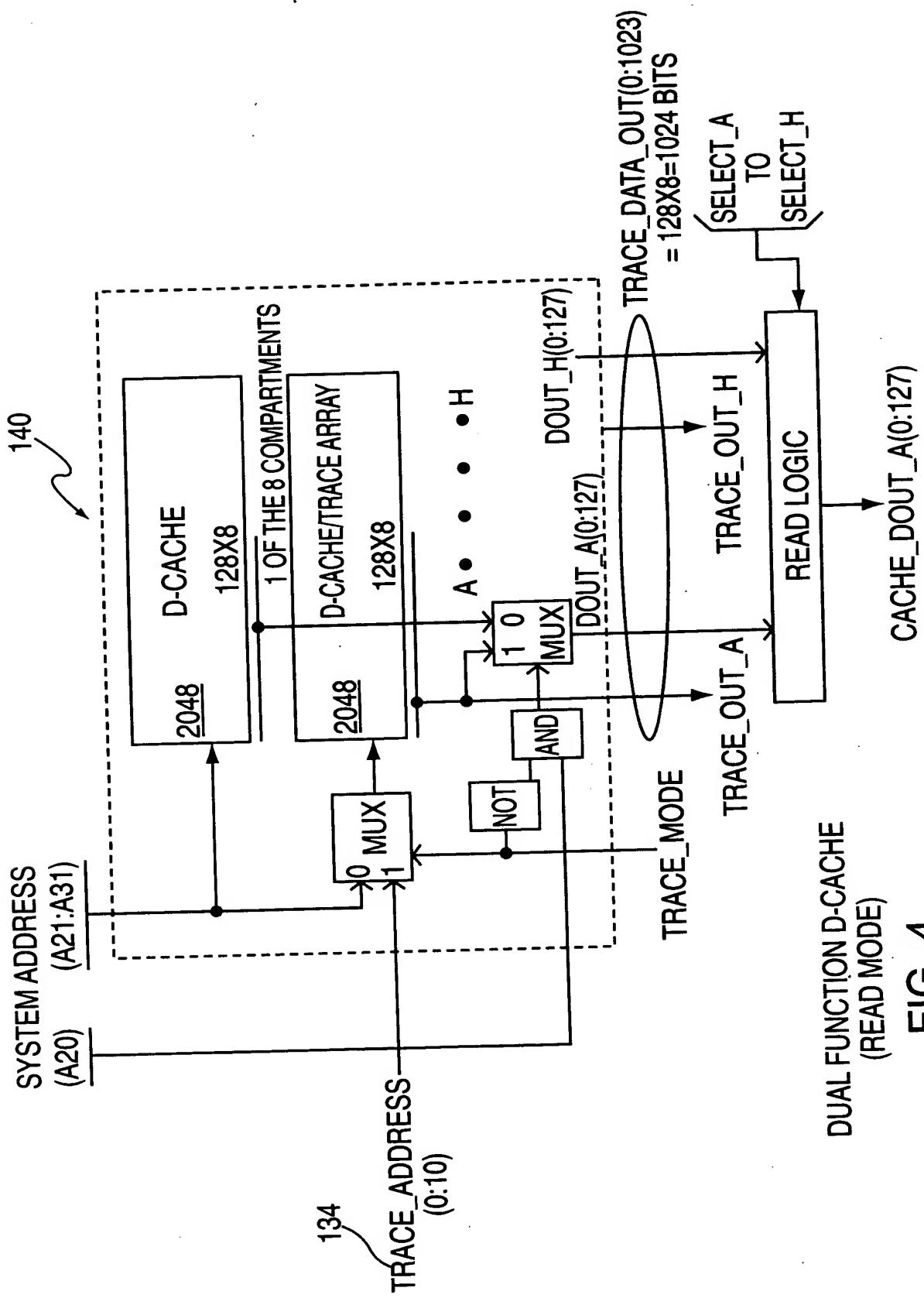
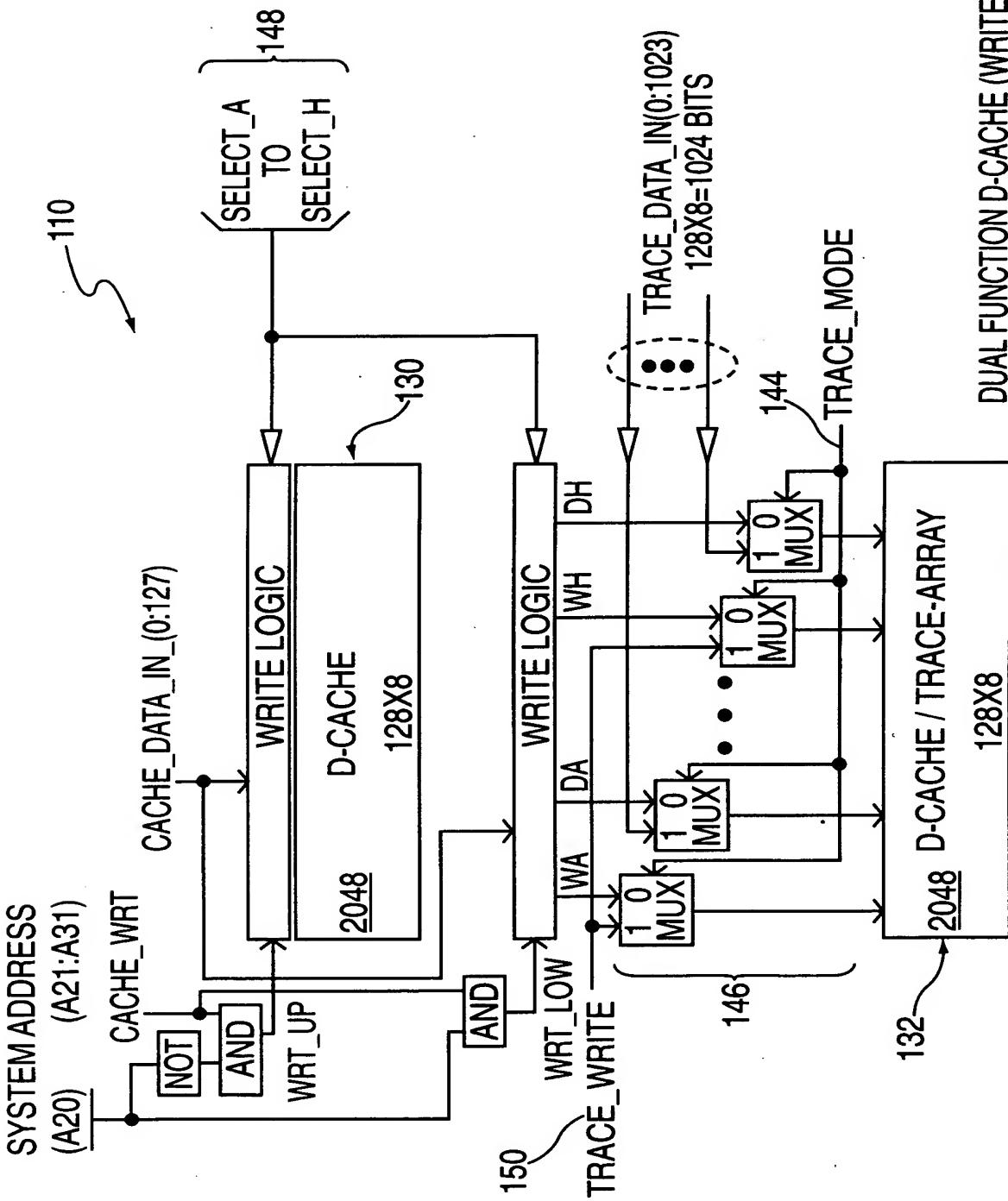


FIG. 3 NORMAL MODE: 512 KB D-CACHE
TRACE MODE: 256 KB D-CACHE AND
256 KB TRACE ARRAY





5
FIG.

DUAL FUNCTION D-CACHE (WRITE MODE).
ADDRESS CONTROL NOT SHOWN
(SAME AS READ MODE)

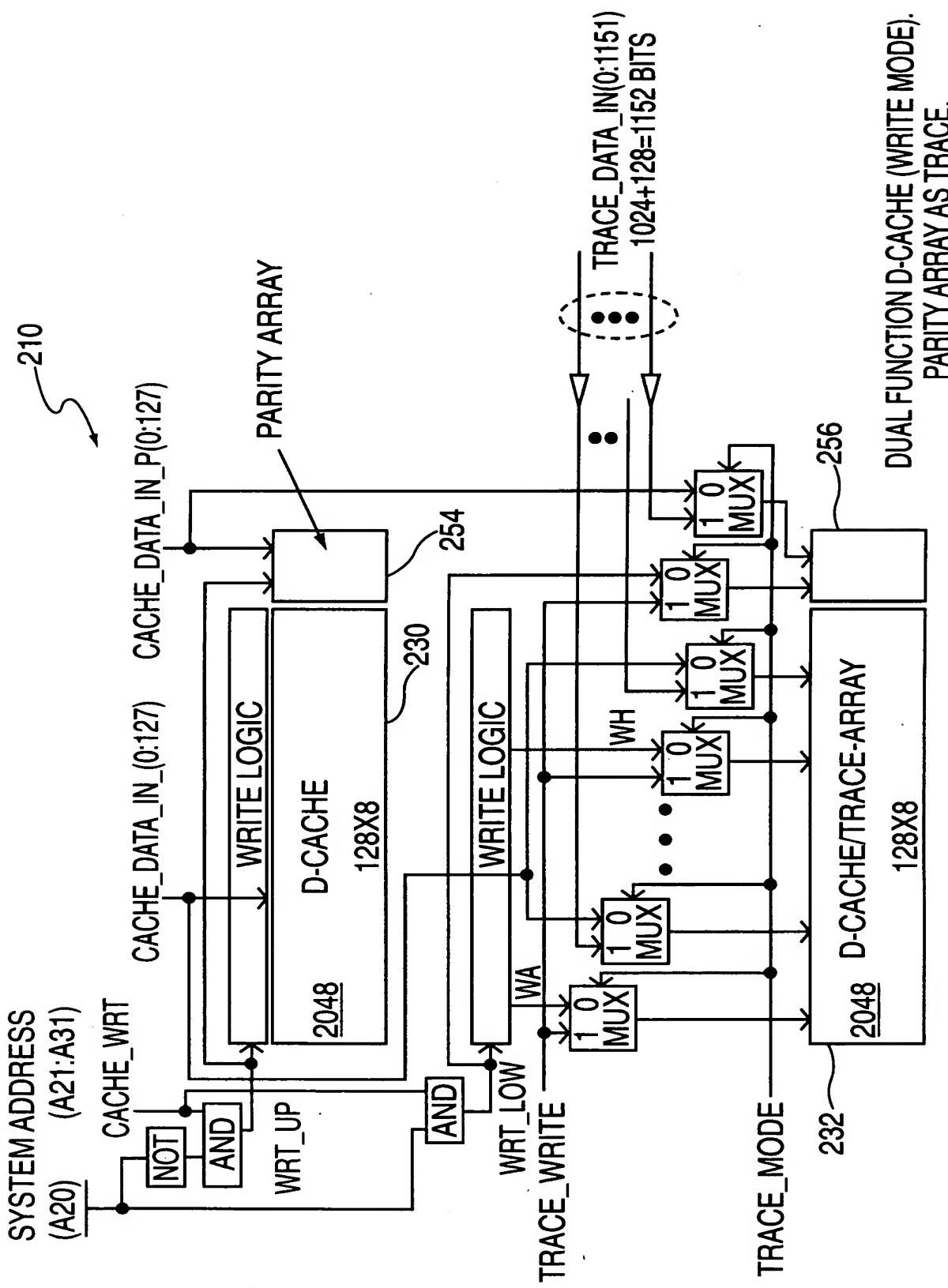
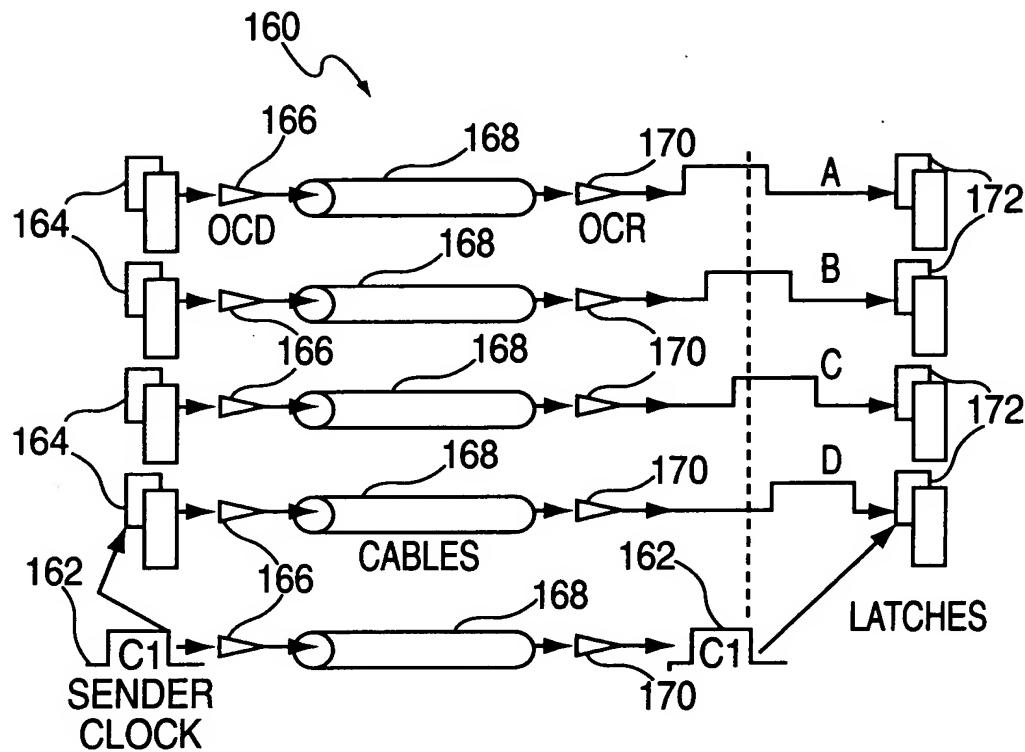
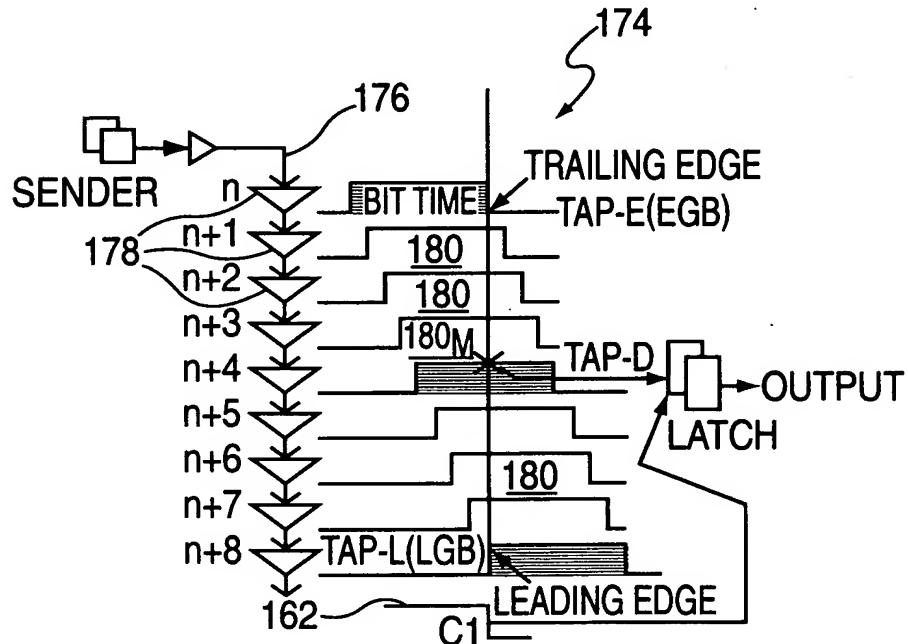


FIG. 6



DATA TRANSMISSION SYSTEM

FIG. 7



PICKING THE CENTERED PHASE

FIG. 8

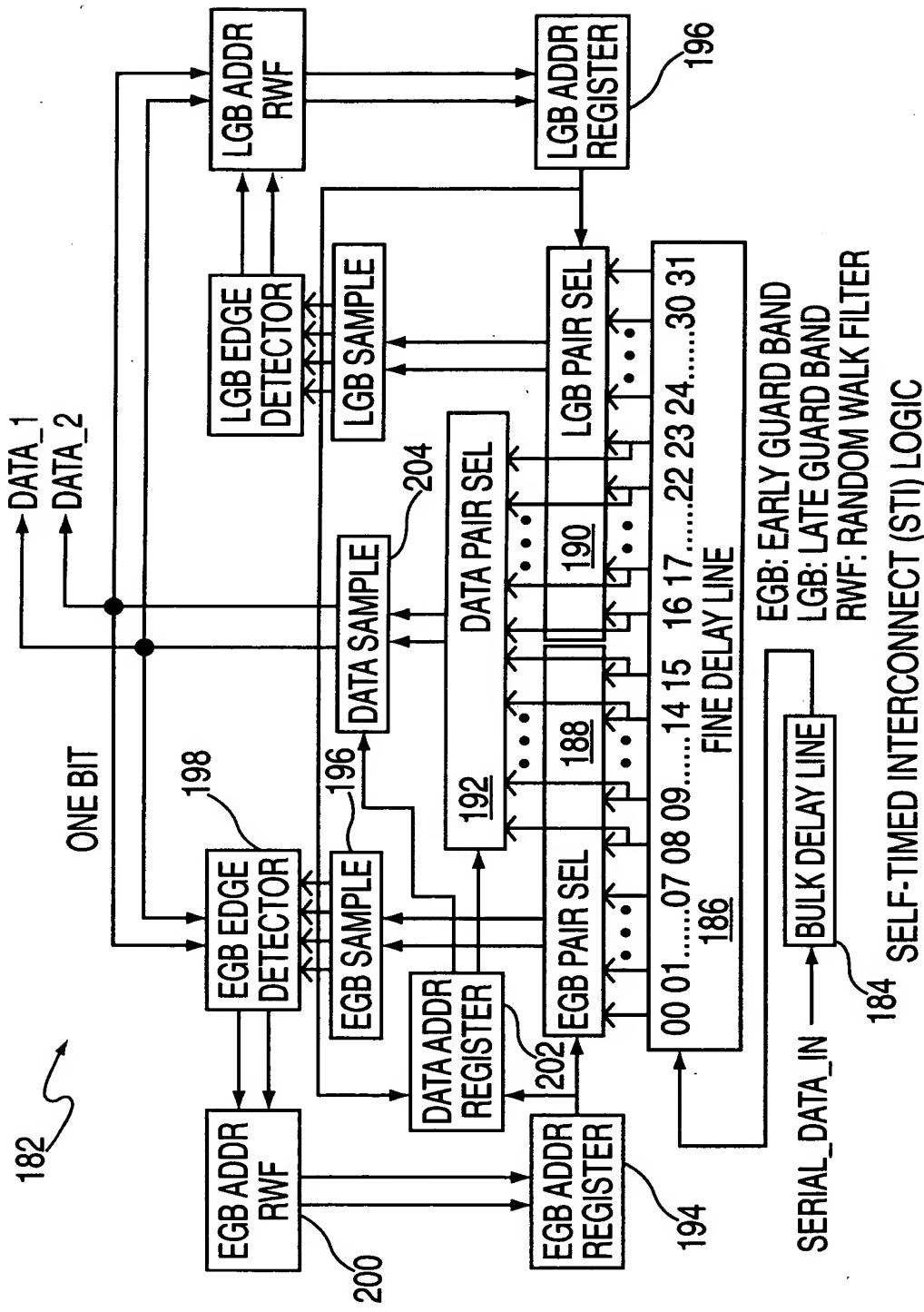


FIG. 9

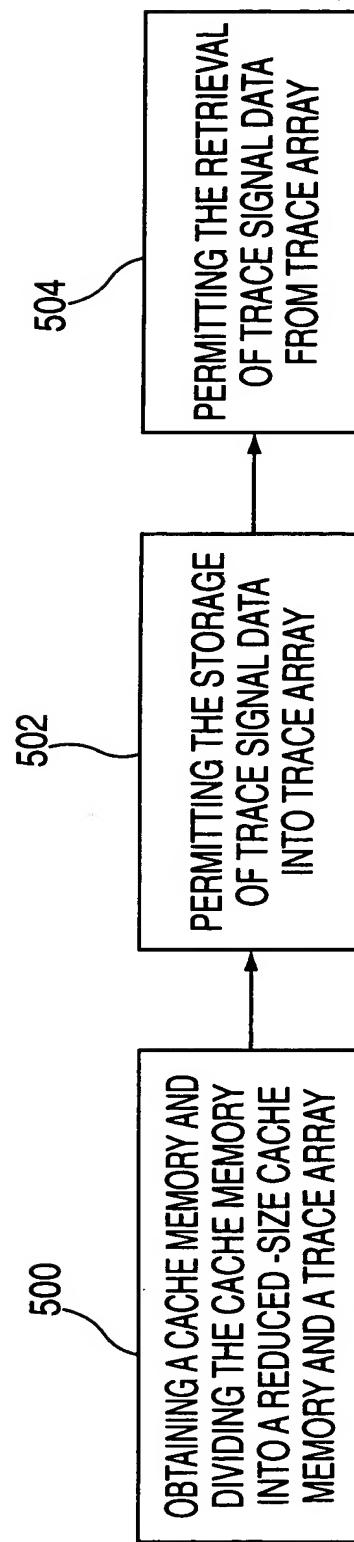


FIG. 10